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wide variety of circuit arrangements
have been proposed for minimizing
the number of pins necessary to
interconnect various circuit
components. As circuit density has
increased, it has become
increasingly important to reduce
the number of pins to enhance the
reliability of various components
and the ability of various
components to interact. In
particular, to reduce the number of
pins necessary to interconnect
different components to one
another, arrangements utilizing
multi-layer wiring are becoming
more common. An example of a

prior art multi-layer wiring technique is illustrated in FIG. 1. An integrated circuit chip 102 is shown including a semiconductor substrate or die which is further encompassed by a passivation layer 0cc13bf012

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.WASHINGTON □ A federal judge

Wednesday ruled that a group of California residents had the right to sue the federal government in challenging its deportation of a 2-year-old girl to Honduras. The decision by a United States District Judge Gladys Kessler, of Federal District Court in San Francisco, means that the state and its residents can press the issue in the current court case, *United States vs. Imelda Maria Leyva*. In that case, the government challenged the right of several residents to sue. “A person... who has no meaningful opportunity to avoid removal has been unlawfully deprived of his or her liberty, and that deprivation may present a compensable injury,” Judge Kessler wrote. “Further, it is by now well established that a violation of due process of law by the federal government may constitute a ‘tort’ under federal

law.” About 45 people have sued the federal government, but Ms. Leyva is the only plaintiff who has moved forward. Judge Kessler said the other plaintiffs were residents of Washington, in addition to California, and not legal residents of the United States, as required by the lawsuit.

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