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A: It is impossible to extract any kind of file

from a.img file. So you need an app that supports the creation of ISO images. You can also use the graphical apps - any data recovery tool will do. The present invention relates to a semiconductor memory device, and more specifically to a synchronous semiconductor memory device and method for selecting a bank address. A synchronous semiconductor memory device receives and processes external commands through an address buffer, an address decoder, a command decoder and a control signal buffer (sometimes called a read/write buffer) which are provided on the semiconductor memory device. The address buffer receives an external address signal, and generates a row address signal and a column address signal. The address decoder receives the row address signal and the column address signal, and generates a select signal for selecting a memory cell in a memory cell array. The command decoder decodes an external command signal, and generates select signals for selecting the read/write buffer, a memory cell, a column address and a row address in the memory cell array and a

peripheral circuit. The read/write buffer receives an external control signal for controlling the memory cell array, a sense amplifier, a precharge circuit and a column address buffer. When an external command signal for accessing a memory cell is input, the read/write buffer controls the precharge circuit and performs the sense amplifying operation according to the control signal. After completing the sense amplifying operation, the read/write buffer controls the column address buffer to generate a column address. The read/write buffer is selected by the command decoder according to the external command signal, and receives an address signal, a command signal and a signal for controlling the memory cell array. An address signal and a command signal are input to the semiconductor memory device through separate pins. The semiconductor memory device is selected according to a bank address stored in a bank address register, and then receives an address signal and a command signal. After receiving a command signal, the semiconductor memory device receives and processes the external command signal

through the read/write buffer. In the semiconductor memory device having a single read/write buffer, a row address is input after a column address is input, and a column address is input after a row address is input. In order to input addresses to the semiconductor memory device, an external address is input first, and then a 6d1f23a050

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